

Taibah University		College of Computers science and Engineering
Computer Organization and Architecture COE 224		Date: 10 Feb 2019
Quiz: 02 Review		Total Marks: 10
Name:		ID:

1. ENIAC Computer is Decimal (Decimal / ~~Binary~~) based computer
2. Contains a word to be stored in memory or sent to the I/O unit, or is used to receive a word from memory or from the I/O unit MBR
3. Specifies the address in memory of the word to be written from or read into the MBR MAR
4. Contains the ISA computer **8-bit opcode instruction** being executed IR
5. Contains the address of the next instruction-pair to be fetched from memory PC
6. The idea of the **stored program concept** is attributed to Von Neumann
7. Organization refers to the operational units and their interconnections that realize the architectural specifications
8. Registers provides storage internal to the CPU
9. In general terms, the four basic functions that a computer can perform Data movement, Data processing, Data storage, Control
10. The way in which the components are interrelated is called structure
11. The operation of each individual component as part of the structure is called function
12. The attributes of a system visible to a programmer is called Architecture
13. AC Employed to hold temporarily operands and results of ALU operations
14. SST It describes fixed-function ICs that have up to ten equivalent gate circuits on a single chip, and they include basic gates and flip-flops
15. Instruction Set contains a list of instructions the microprocessor recognizes and carries out a specific operation such as addition, multiplication, etc. intended by the programmer
16. A bus is a common group of wires that interconnect components in a computer system
17. Address bus is unidirectional bus used to transmit the address of a location in memory or I/O parts in the data transfer
18. A microprocessor executes a program by repeatedly cycling through the following three steps: fetch, decode, execute
19. The advantage of a wider data bus is Speed
20. Increasing the address bus size indicates that the memory capacity is increasing

Handwritten signature