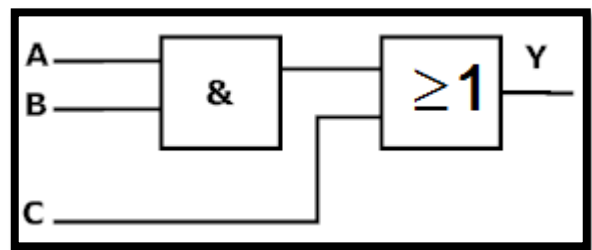


Find the Boolean function of the circuit

Draw the logic diagram

2. A three-input AND-OR (AO) gate shown in Figure below produces a TRUE output if both A and B are TRUE, or if C is TRUE.

Complete the truth table for the function



Write down the Boolean function

3. Write a Boolean equation for each of the truth tables in Figure below, and draw the logic diagram

INPUT			OUTPUT
A	B	C	Y
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	1

INPUT				OUTPUT
A	B	C	D	Y
0	0	0	0	1
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	1
1	0	0	1	0
1	0	1	0	1
1	0	1	1	0
1	1	0	0	0
1	1	0	1	0
1	1	1	0	1
1	1	1	1	0

4. Find the truth table, and draw the logic diagram

$F = \overline{X + Y}$	$F = \overline{X} + \overline{Y}$
$F = \overline{XY}$	$F = \overline{XY}$