

Taibah University		College of Computers science and Engineering
Computer Organization and Architecture COE 224		Date: 19 Feb 2019
Quiz: 03		Total Marks: 10
Name:		ID:

1. A benchmark program is run on a 40 MHz processor. The executed program consists of 100,000 instruction executions, with the following instruction mix and clock cycle count:

Instruction Type	Instruction Count	Cycles per instruction
Integer arithmetic	45000	1
Data transfer	32000	2
Floating point	15000	2
Control transfer	8000	2

Determine the effective **CPI, MIPS rate, and execution time** for this program.

2. Complete


